

Notice of References Cited	Application/Control No. 09/992,076	Applicant(s)/Patent Under Reexamination NEMECEK, CRAIG	
	Examiner Jason Proctor	Art Unit 2123	Page 1 of 2

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
*	A	US-5,371,878	12-1994	Coker, Robert T.	703/28
	B	US-			
	C	US-			
	D	US-			
	E	US-			
	F	US-			
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Stan Augarten, "The Chip Collection - Introduction - Smithsonian Institute", "STATE OF THE ART", "The First 256-Bit Static RAM", retrieved on 11/14/2005 from http://smithsonianchips.si.edu/augarten/p24.htm
	V	"POD - Piece Of Data, Plain Old Documentation, Plain Old Dos, ...", retrieved on 11/14/2005 from http://www.auditmypc.com/acronym/POD.asp
	W	"Pod - Wikipedia, the free encyclopedia", retrieved on 11/14/2005 from http://en.wikipedia.org/wiki/Pod
	X	"pod - definition by dict.die.net", retrieved on 11/14/2005 from http://dict.die.net/pod

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.

Notice of References Cited	Application/Control No. 09/992,076	Applicant(s)/Patent Under Reexamination NEMECEK, CRAIG	
	Examiner Jason Proctor	Art Unit 2123	Page 2 of 2

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-			
	B	US-			
	C	US-			
	D	US-			
	E	US-			
	F	US-			
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	"In Circuit Emulators - descriptions of the major ICEs around", retrieved on 11/14/2005 from http://www.algonet.se/~staffann/developer/emulator.htm
	V	
	W	
	X	

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.